

11/16/01  
JC945 U.S. PTO

11-23-01

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LAW OFFICES OF  
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INTELLECTUAL PROPERTY ATTORNEYS

INTERNET: www.IP-Counsel.com

JC971 U.S. PTO  
09/09/01

11/16/01

November 16, 2001

Box Patent Application  
Commissioner for Patents  
Washington, D.C. 20231

Attorney Docket No.: 023-0013

Transmitted herewith for filing is a patent application as follows:

Inventor(s): Roy E. Scheuerlein  
Title: INTEGRATED CIRCUIT MEMORY ARRAY WITH FAST TEST MODE  
UTILIZING MULTIPLE WORD LINE SELECTION AND METHOD  
THEREFOR  
Assignee: Matrix Semiconductor, Inc.

Enclosed are:

- ☒ Application Data Sheet (2 page(s))
- ☒ Request and Certification under 35 U.S.C. 122(b)(2)(B)(i) (1 page(s))
- ☒ 37 Pages of Written Description (including Specification, Claims and Abstract)
- ☒ 9 Sheets of Drawings, ☐ Formal / ☒ Informal
- ☒ Declaration for Patent Application (2 pages), ☒ Executed / ☐ Unexecuted
- ☒ Assignment of the Invention (3 pages, including Cover Sheet)
- ☒ Other: Check in the amount of \$2,064.00
- ☒ This Transmittal Letter (in duplicate) (1 page(s)) ☒ Return Postcard

CLAIMS AS FILED

	Number Filed	Number Extra	Rate	Fee
Basic Fee =				740.00
Total Claims	68 - 20	= 48	x \$18.00 =	864.00
Independent Claims	8 - 3	= 5	x \$84.00 =	420.00
Multiple Dependent Claims (if any) - \$280.00 fee				0.00
Other: Assignment Filing Fee				40.00
TOTAL FILING FEE				\$2,064.00

- ☐ Small entity status is entitled to be asserted for the application.
- ☒ A check is enclosed for the Total Filing Fee shown above.
- ☐ Please charge the Total Filing Fee shown above to Deposit Account 50-0631.
- ☒ The Commissioner is hereby authorized to charge any additional fees under 37 C.F.R. § 1.16 or 1.17 that may be required during the pendency of this application, and to similarly credit any overpayment, to Deposit Account 50-0631.

EXPRESS MAIL LABEL NO.:

EL684226339US

Respectfully submitted,



Andrew C. Graham, Reg. No. 36,531  
Attorney for Applicant(s)  
(512) 347-9030  
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Roy E. Scheuerlein  
Title: INTEGRATED CIRCUIT MEMORY ARRAY WITH FAST TEST MODE  
UTILIZING MULTIPLE WORD LINE SELECTION AND METHOD  
THEREFOR  
Application No.: (unassigned) Filed: Herewith  
Examiner: (unassigned) Group Art Unit: (unassigned)  
Atty. Docket No.: 023-0013

November 16, 2001

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COMMISSIONER FOR PATENTS  
Washington, DC 20231

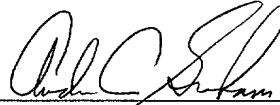
**REQUEST AND CERTIFICATION**  
***under 35 U.S.C. § 122(b)(2)(B)(i)***

I hereby certify that the invention disclosed in the attached application has not and will not be the subject of an application filed in another country, or under a multilateral agreement, that requires publication at eighteen months after filing. I hereby request that the attached application not be published under 35 U.S.C. § 122(b).

EXPRESS MAIL LABEL NO.:

EL684226339US

Respectfully submitted,



Andrew C. Graham, Reg. No. 36,531  
Attorney for Applicant(s)  
(512) 347-9030  
(512) 347-9031 (fax)

*This request must be signed in compliance with 37 CFR 1.33(b) and submitted with the application upon filing.*

*Applicant may rescind this nonpublication request at any time. If applicant rescinds a request that an application not be published under 35 U.S.C. 122(b), the application will be scheduled for publication at eighteen months from the earliest claimed filing date for which a benefit is claimed.*

*If applicant subsequently files an application directed to the invention disclosed in the attached application in another country, or under a multilateral international agreement, that requires publication of applications eighteen months after filing, the applicant must notify the United States Patent and Trademark Office of such filing within forty-five (45) days after the date of the filing of such foreign or international application. Failure to do so will result in abandonment of this application (35 U.S.C. 122(b)(2)(B)(iii)).*